T Rohit Reddy

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Objective

To work in a challenging environment where I can apply and enhance my knowledge. My keen interests are in the fields of design and VLSI.

Education

Academic Qualifications.....

Indian Institute of Technology Hyderabad

Electrical Engineering with minor in Entrepreneurship, CGPA 6.84*

2012–present

Sri Chaitanya Junior CollegeHyderabadAP BIE, Maths(M), Physics(P) and Chemsitry(C), 93.9%2010–2012

St. Joseph's High School

AP SSC Board, 87.6%

Hyderabad

2010

Academic Achievements.....

- Secured AIR 2416 in the prestigious IIT-JEE 2012 (99.6 percentile).
- Scored 99.33 percentile among 12 lakh applicants in AIEEE 2012 examination.
- Secured a rank 669 in EAMCET-2012.

Core Courses....

- VI Semester: Digital IC Design and Verification, Nano Materials, Power systems Practice, Machine Learning
- V Semester: ElectroMagnetic Field Theory, Digital Communications, Digital Circuits, Semiconductor Devices and Modeling, Power Electronics
- o IV Semester: Control Systems, Analog Circuits, Computer and Microprocessor Organization
- III Semester: Digital Signal Processing, Probability and Random Processes, Digital System Design, Electrical Machines
- o II Semester: C-Language, ElectroMagnetic Circuits

Course work in Entrepreneurship.....

- Strategic Innovative Entrepreneurship*
- o Introduction to Business Plan*
- Early Customer Acquisition and Relationship Management*
- Introduction to Sales and Marketing
- Introduction to Finance and Economy
- Introduction to Entrepreneurship

Projects

• CORDIC based Single Channel ICA implementation on chip (January'15 - present)

Guide: Dr. Amit Acharyya

Developing a preprocessor for whitening the observed signals of an EEG through Eigen Value Decomposition of the mixing matrix using CORDIC and therefore implementing the Single Channel Independent Component Analysis using the same architecture, CORDIC.

• Seeing the Arrow of Time (January'15 - present)

Guide: Dr. C K Mohan

Currently working on three different types of feature extraction in Videos and training an SVM Classifier with the features extracted to detect whether the direction of time in the video is going forwards or backwards.

• Study of Adaptive Antenna Array Properties using Genetic Algorithm (August'14 - November'14)
Guide: Dr. Zafar Ali Khan

Using Genetic Algorithm, simulations are made for different number of generations, population in the Genetic Algorithm and tested it for different dimensional Adaptive Antenna Arrays such as 3x3, 3x4, 3x5...and made a report on the results.

• **Ph.D Portal of IIT Hyderabad** (May'14 - August'14)

Online Research Platform for IIT Hyderabad is quintessential to display the ongoing research activities and for better exposure to both industry and academia. Research Portal was developed by a team of 3 using Wordpress CMS and Bootstrap UI Framework.

- **Others:**(*August'12 April'13*)
 - 1. Electronic Door Lock System
 - 2. Fuel Efficiency
 - 3. C language based Library Management System
 - 4. Heritage City project

Technical and Personal skills

- **Programming Languages:** Proficient in: C, C++, Matlab, VHDL, Verilog HDL, HTML, CSS, Javascript. Also basic ability with: PHP, MySQL, LaTeX.
- o Industry Software Skills: Xilinx (Advanced), Matlab (Advanced), ModelSim (Intermediate), Pspice/LTspice (Intermediate), Most MS Office products (Intermediate).
- **General Business Skills:** Good presentation skills, Works well in a team.

Positions of Responsibilities and other activities

- o Summer Intern at Sciensation Media, 2013
- Co-ordinator of Photography club 2013-14, IIT Hyderabad
- o Core Member of Photography Club 2012-13, IIT Hyderabad
- o Organiser of Loose Your Feet and Pixel Adda in ELAN 2014
- o Core member of Design Club (August- December'14), IIT Hyderabad
- $\circ~$ Core Member of organizing team, Fresher's Night 2014
- Table tennis NSO player of IIT Hyderabad.
- Volunteer of Grow N Shine (A Social Initiative), 2012-13
- Volunteer of Student Gymkhana 2012-13

Declaration

I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.